

Figure 3

Sensor Array overlaid with Bayer pattern

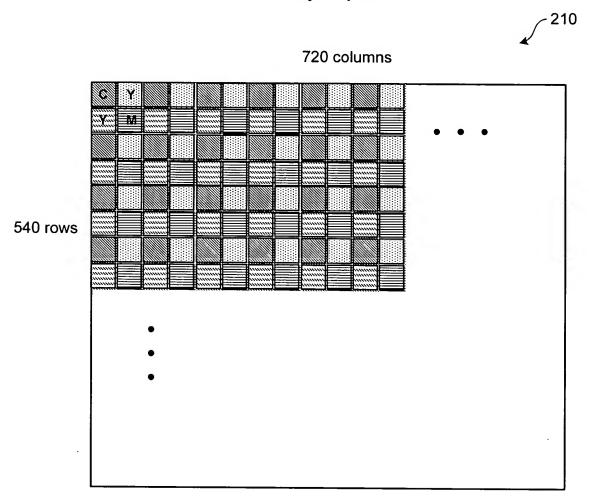


Figure 4

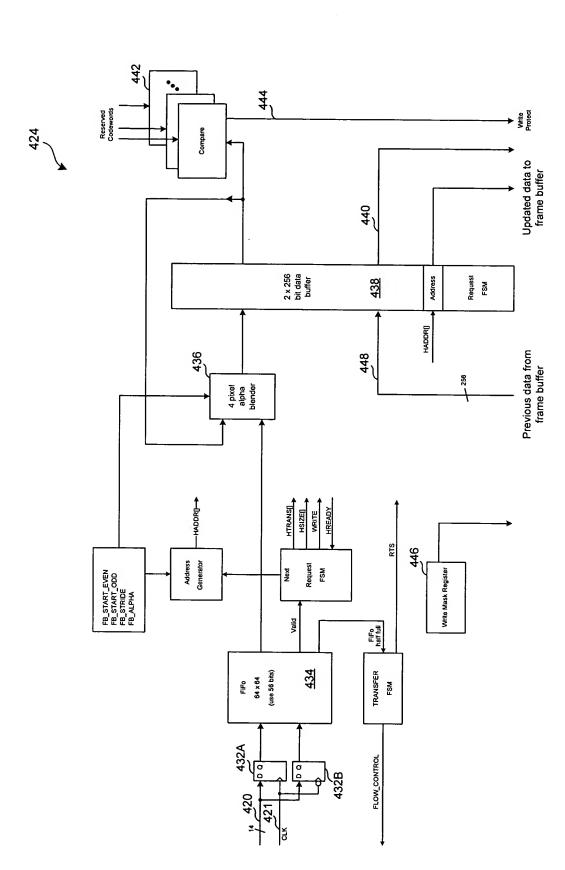
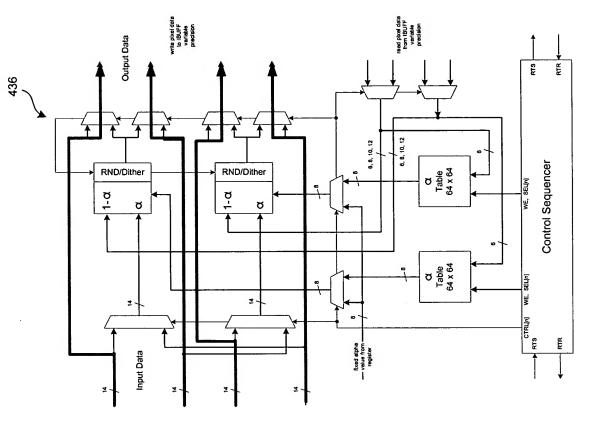


Figure 5

Title: Image Processor with Noise Reduction Circuit Inventor: Michael Frank Attorney Docket No: PIX-P-053

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write pixel data to IBUFF variable precision Output Data 6, 8, 10, 12 Control Sequencer Q Table 64 x 64 RND/Dither RND/Dither ರ Ø α Table 64 x 64 WE, SEUN ፯ **z** -RTR Input Data P2-7

write pixel data to IBUFF variable precision

RND/Dither

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4 .

Output Data

RND/Dither

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Input Data

436

α Table 64 x 64

CA Table 64 x 64

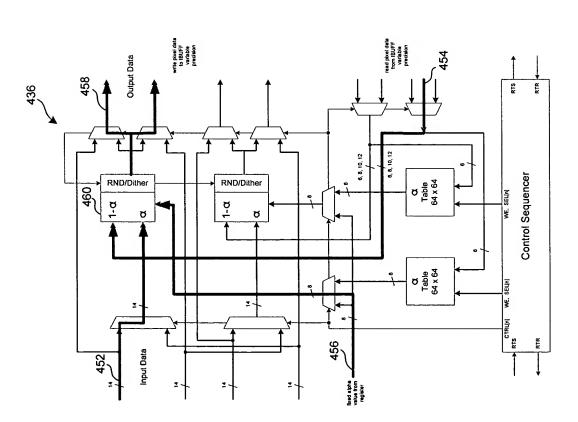


Control Sequencer

WE, SELIN

CTRLIN

RTR



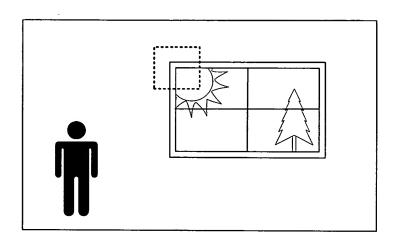
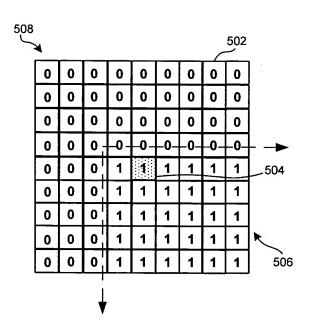


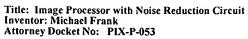
Figure 10



Blending Mask								510
.2	.2	.2	.2	.2	.2	.2	.2	.2
.2	1	1	1	1	1	1	1	.2
.2	1	2	2	2	2	2	1	.2
.2	1	2	4	4	4	2	1	.2
.2	1	2	4	8	4	2	1	.2
.2	1	2	4	4	4	2	1	.2
.2	1	2	2	2	2	2	1	.2
.2	1	1	1	1	1	1	1	.2
.2	.2	.2	.2	.2	.2	.2	.2	.2

Figure 13

Figure 14



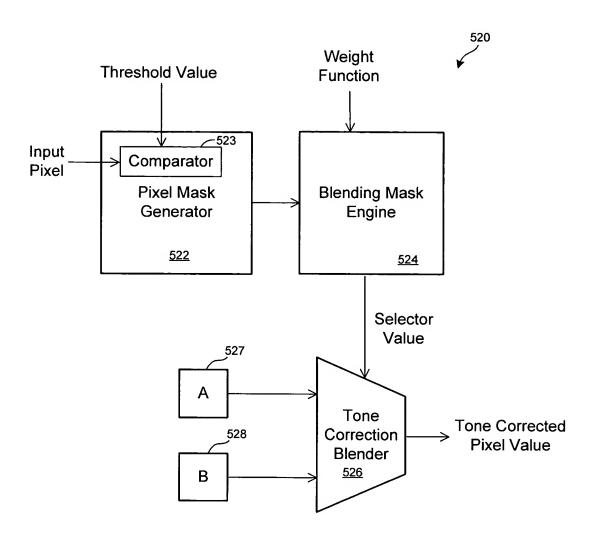


Figure 11

